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2009 IEEE International Symposium on Precision Clock Synchronization for Measurement, Control and Communication

Plug-Fest October 12 - 13, 2009
Symposium October 14 - October 16, 2009
University of Brescia
Brescia, Italy

Aim: The objective of the symposium is to provide a forum for researchers and practitioners from industry, academia, and government involved in the area of **Precision Clock Synchronization** using IEEE 1588 or similar techniques and protocols.

Symposium topics: We welcome paper submissions on all areas of precision clock synchronization. Example topics of interest include:

Design, usage and research concerning IEEE 1588	Performance test and evaluation tools
Implementation design and performance issues	Analytic, modelling, and simulation studies
System and device level studies and system design issues	System management
Servo design for slave clocks	System Security
Device design	Gigabit and fiber support, wireless networks.
Components and embedded implementations	Applications & requirement investigations
Conformance testing and system integration issues	APIs, object models, programming models
Design environments and tools for applications	Work-in-Progress reports

Plug-fest: The plug-fest is an opportunity to test and demonstrate device and system interoperability. Implementers are invited to cooperate with others and address compatibility issues in order to demonstrate system-wide clock synchronization and operation of protocols across a wide spectrum of devices. The plug-fest will take place prior to the symposium on October 12-13, 2009. Prospective participants in the plug-fest should submit a letter of intent to participate and an abstract of one page or less describing the devices, applications and the technical issues illustrated. Submission details are available at: http://www.ispcs.org.

Exhibitions and Tutorials: Sponsoring companies are invited to exhibit their products/prototypes. Exhibition space will be provided during the conference, allowing discussions with sponsors during the breaks. Details on sponsoring and tutorials on IEEE 1588 are available at: http://www.ispcs.org.

Submission of Papers: Authors should submit a four-page extended abstract describing the paper's contents. Templates and online submission of abstracts will be available at: http://www.ispcs.org. Students are welcome to submit papers.

Work in Progress Papers: Work in Progress (WiP) papers will be reviewed and presented in a poster session.

Student Paper Award: Details on submitting papers for best student paper award will be available at http://www.ispcs.org.

Paper Acceptance: Extended abstracts will be peer-reviewed. Each accepted paper must be presented at the conference by one of the authors. The final manuscript must be accompanied by the registration form and proof of payment. All conference and plug-fest attendees, including authors and session chairpersons, must pay the conference registration fee and their own travel expenses.

Final Papers: Authors of accepted abstracts are encouraged to submit a full paper. Final papers are limited to **six pages or less in IEEE double column format**. Full papers are accepted in either PDF or postscript format. All full papers will be published in the conference proceedings in CD and on IEEE Xplore.

Submission Schedule:

Four-page extended abstracts for papers	May 11, 2009
Letters of intent for plug-fest	June 8, 2009
Acceptance notice and WiP paper Submission deadline	June 22, 2009
Final written materials to be published in proceedings	August 17, 2009

http://www.ispcs.org